

<b>Form PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>	Docket Number 247322001700	Application Number 09/424,966
	Applicant Akihide SHIBATA and Hiroshi IWATA	
	Filing Date March 9, 1998	Group Art Unit 5611
	Mailing Date March 2, 2000	

## U.S. PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate
Loke	1	6/24/97	08/881,697	Hiroshi IWATA, et al.			

## FOREIGN PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO
Loke	2.	8/2/91	3-178170	JAPAN			
	3.	3/8/88	63-53967	JAPAN			
	4.	4/8/91	03-082151	JAPAN			
	5.	4/27/92	4-125960	JAPAN			
✓	6.	4/28/94	6-120439	JAPAN			
Loke	7.	11/27/97	PCT/JP97/04344	PCT			

## OTHER DOCUMENTS

(including author, title, Date, Pertinent Pages, Etc.)

Examiner Initials	Ref. No.	Title
Loke	8.	F. Assaderaghi et al., IEDM 94, pages 809-812 "A Dynamic Threshold Voltage MOSFET (DTMOS) for Ultra-Low Voltage Operation"

EXAMINER: (examiner)

Loke

DATE CONSIDERED:

4/2/02

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.